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| Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | | Complete if Known | | |
| | | | Application Number | 10/517,838-Conf. #1333 | |
| | | | Filing Date | December 15, 2004 | |
| | | | First Named Inventor | Yoji ARAMAKI | |
| | | | Art Unit | 2817 | |
| | | | Examiner Name | Not Yet Assigned | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 1163-0516PUS1 |

| U.S. PATENT DOCUMENTS | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Number-Kind Code ² (if known) | | | |
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| KG | BA | JP-61-52002-A | 03-14-1986 | | | |

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| NON PATENT LITERATURE DOCUMENTS | | | | |
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| Examiner Initials | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | T ² |
| KG | CA | "ADVANCED COMPUTER DESIGN FOR A HIGH PERFORMANCE COMPACT ORTHO-MODE TRANSDUCER"; Antennas and Propagation Society International Symposium; G. Bertin et al.; IEEE; Vol. 4; June 21, 1998; pages 2254-2257 | | |

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/Kimberly Glenn/ (09/01/2006)

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Birch, Stewart, Kolasch & Birch, LLP

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| KG ↓ ↓ ↓ ↓ | CA | "ANALYSIS AND DESIGN OF BROADBAND THIN OMT WITH TURNSTILE JUNCTION", Yoji ARAMAKI et al., The Institute of Electronics, Information and Communication Engineers, General Conference 2003, March 2003, C-2-44, P. 76. | | | |
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